

FIG. 1

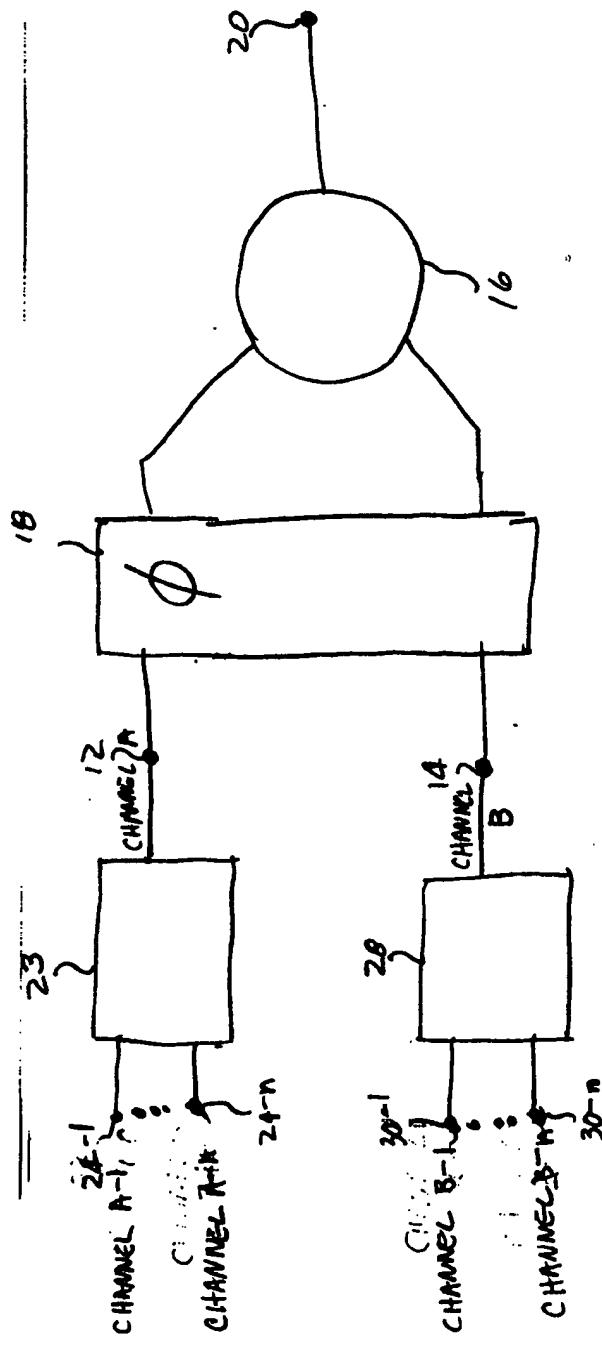


FIG. 2a

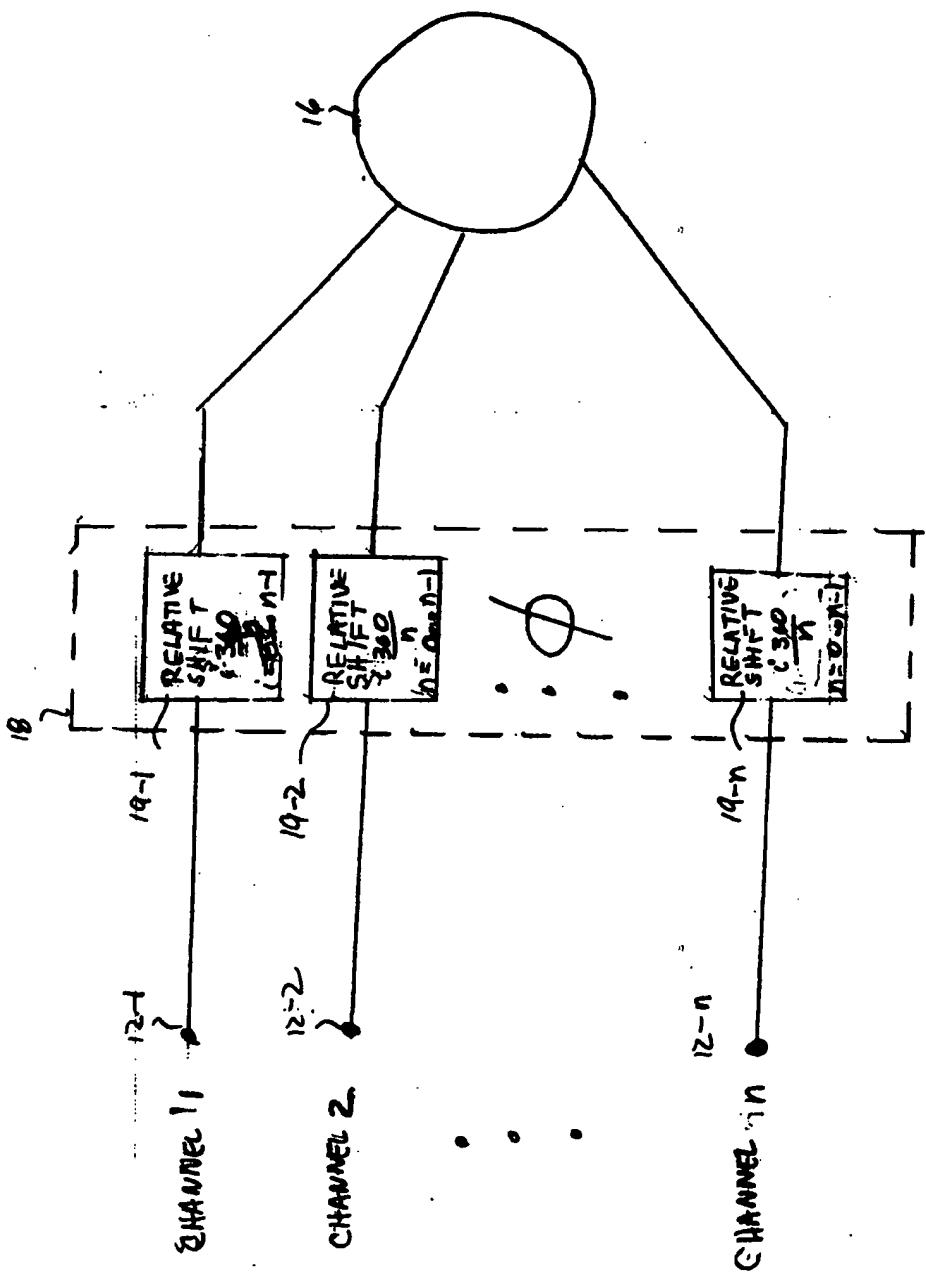


Fig. 2b

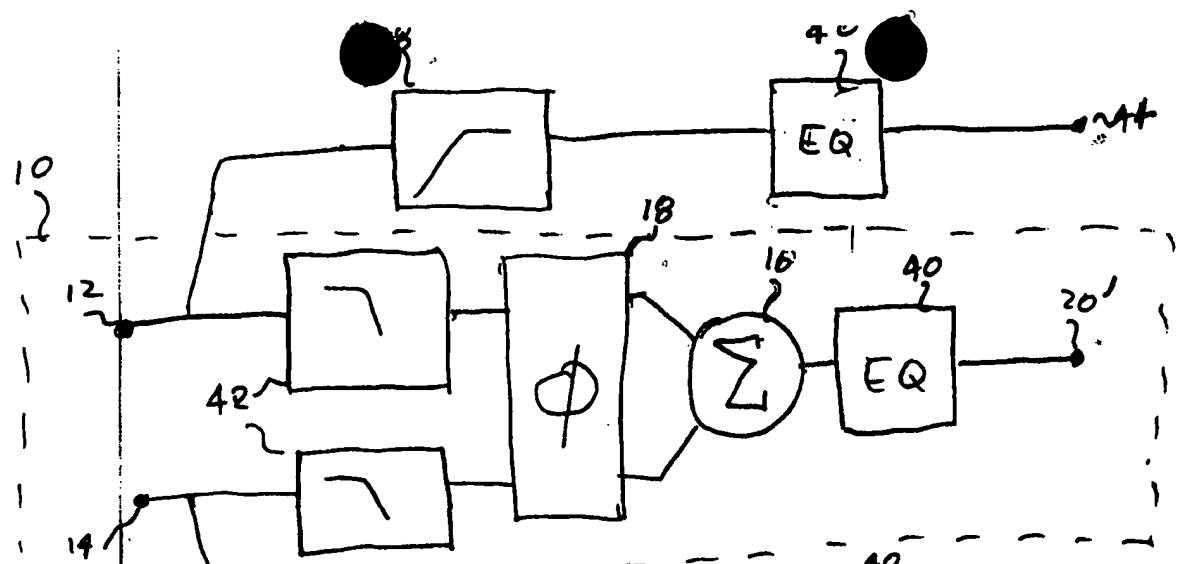


FIG. 3a

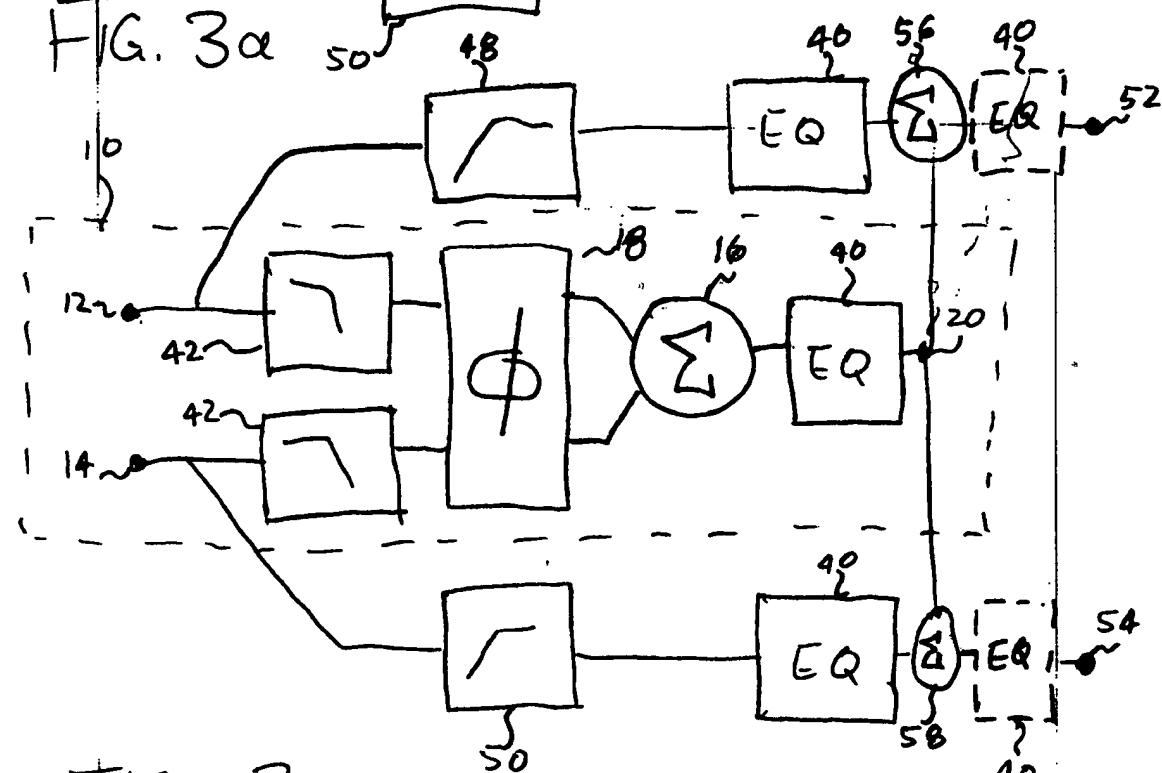


FIG. 3b

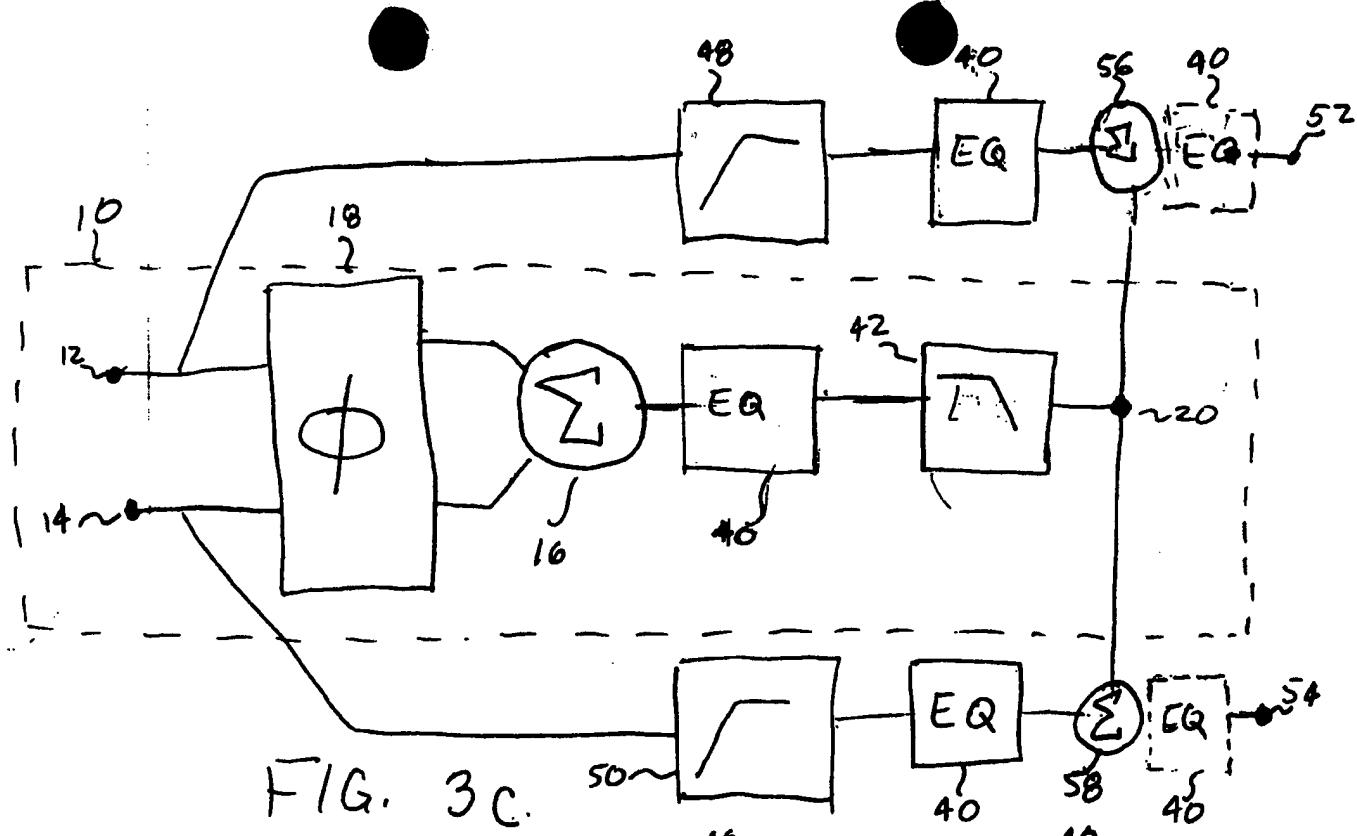


FIG. 3c.

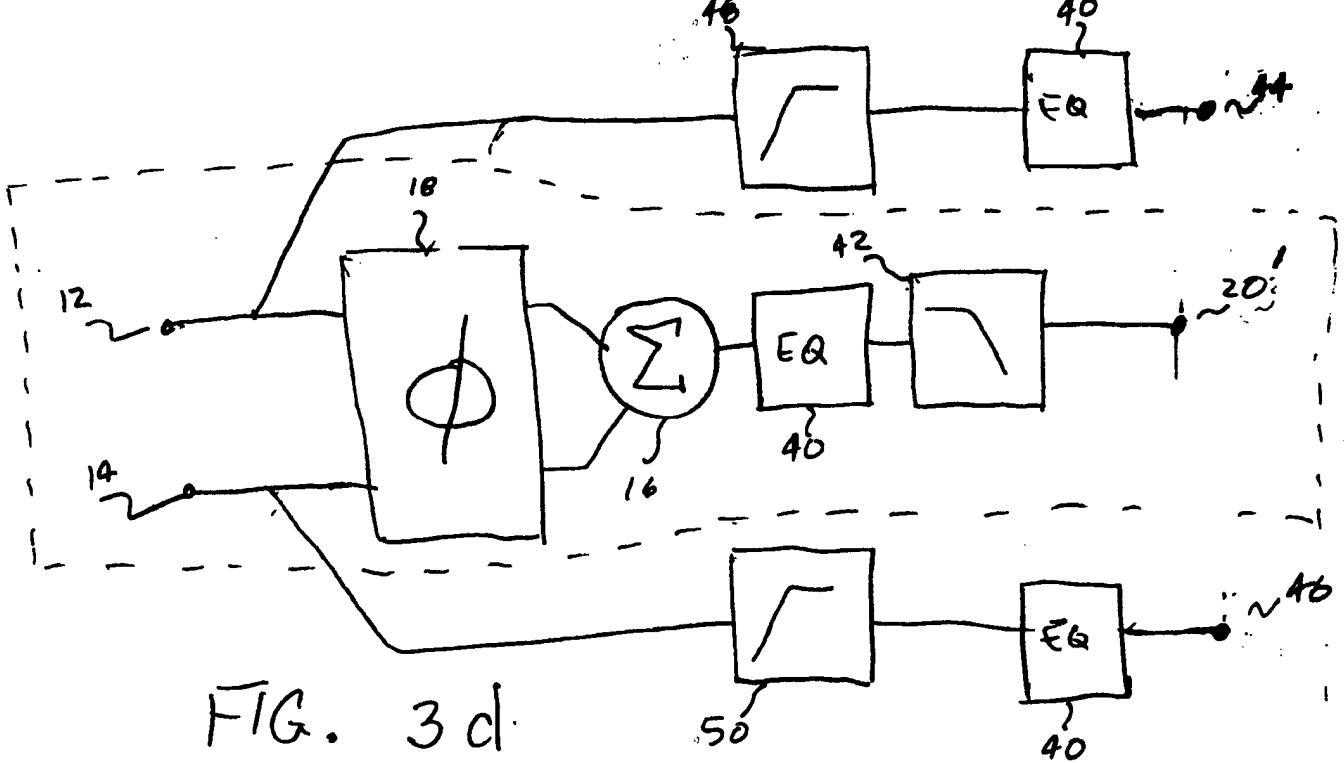
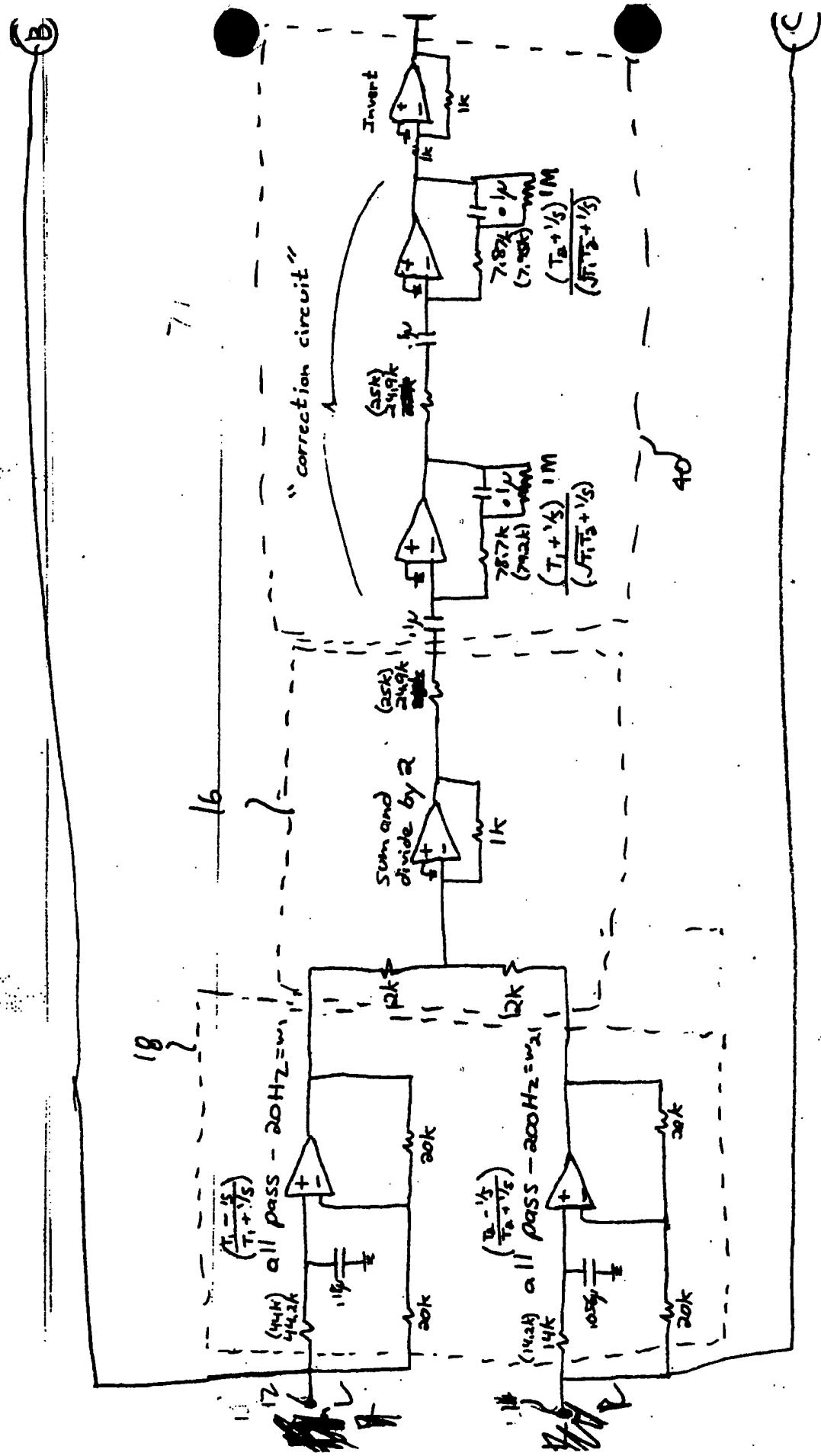


FIG. 3d.



E/G<sub>4</sub> 4a

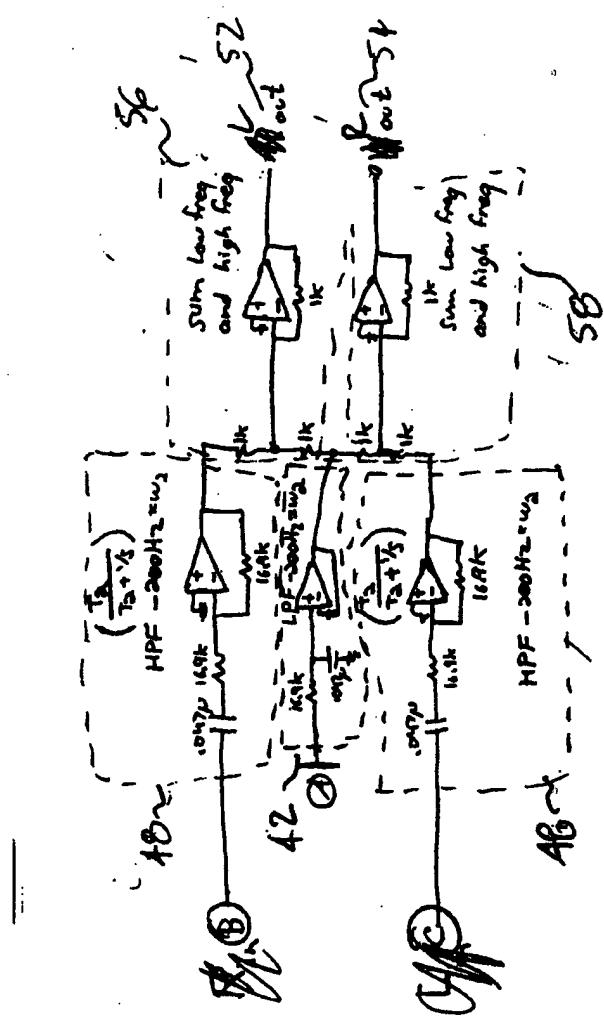


FIG. 4b.

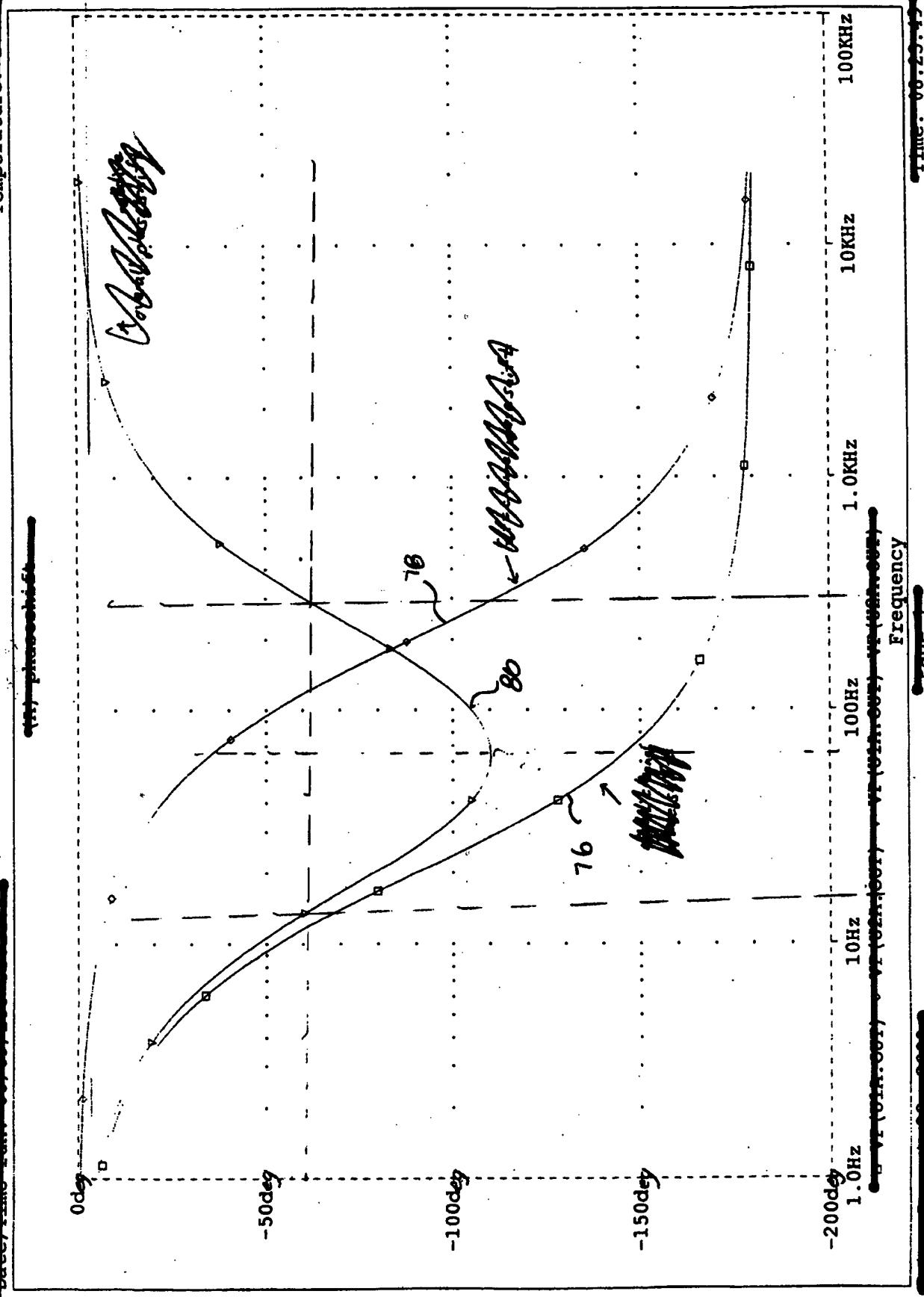


FIG. 5a

~~"MAGNITUDE RESPONSE AT NODE "D" OF FIG. 4A"~~

~~"MAGNITUDE RESPONSE OF FIGURE 4A"~~

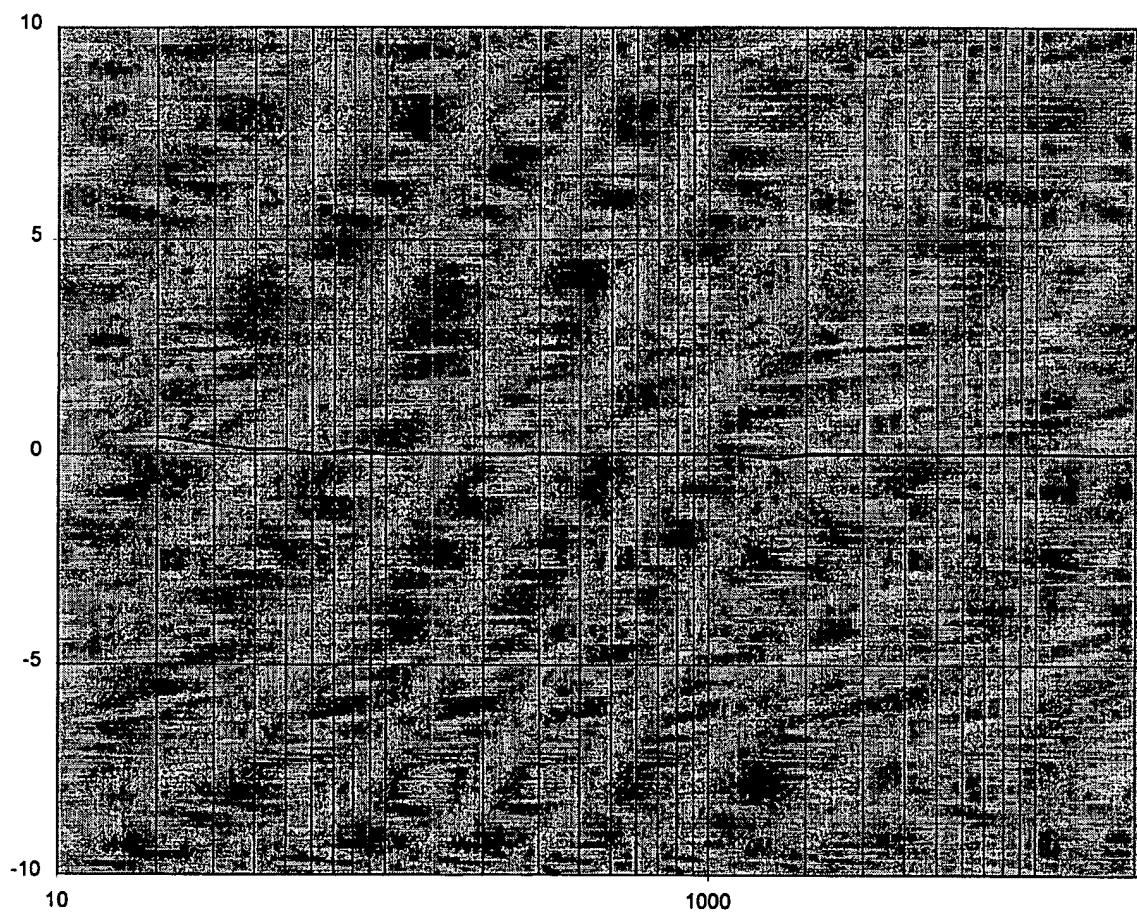


FIG. 5b

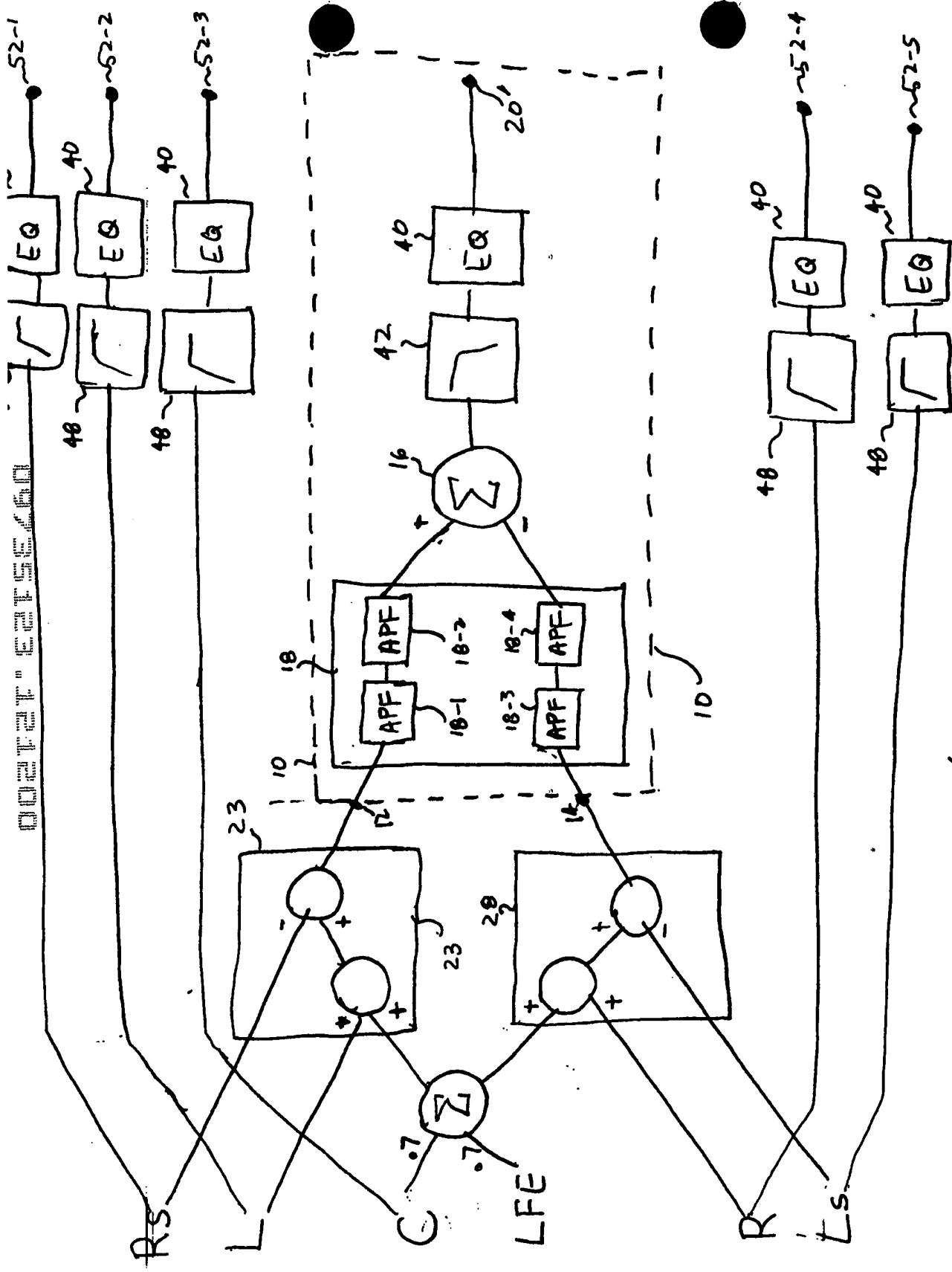


FIG. 6

## **NORMALIZED RELATIVE PHASE-DIFFERENCE OF ALL-PASS NETWORKS REALIZED BY DIGITAL SIGNAL PROCESSING MEANS**

## ~~NORMALIZED RELATIVE PHASE DIFFERENCE OF ALL-PASS-NETWORKS~~

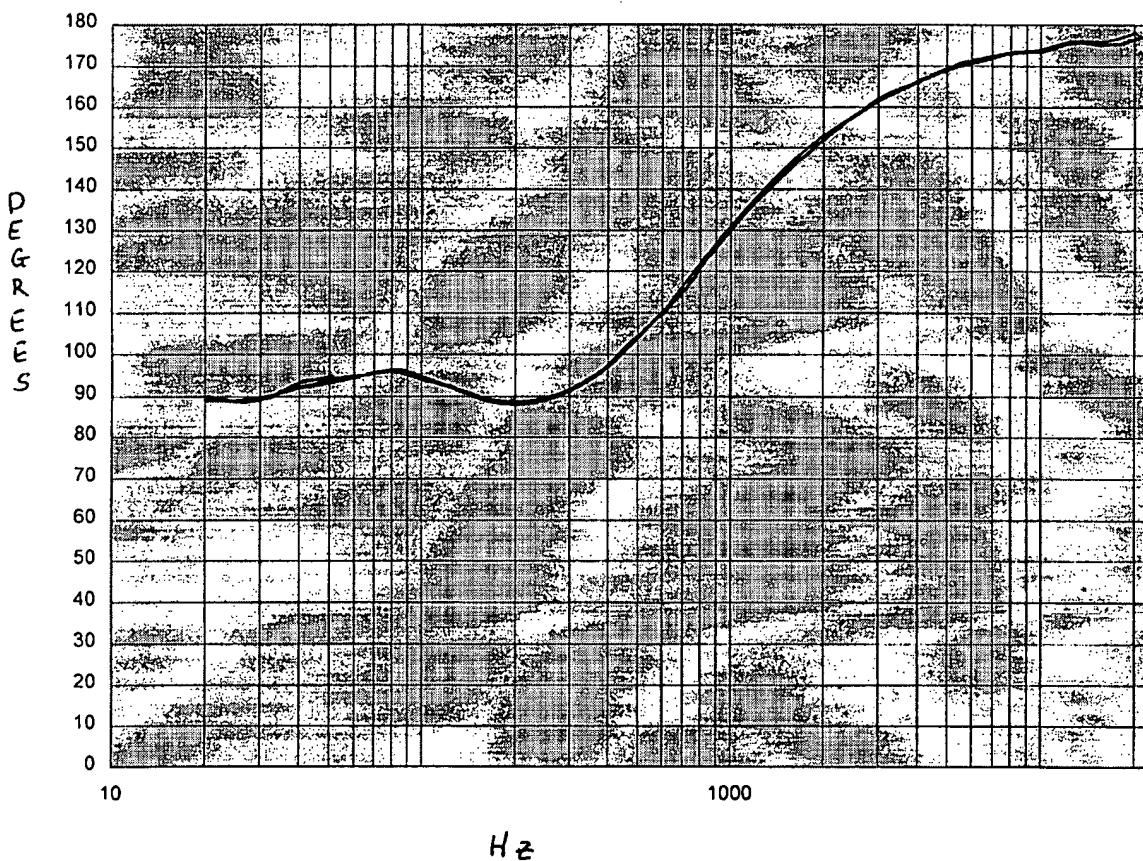


FIG. 7a

~~MAGNITUDE RESPONSE OF DIFFERENTIALLY COMBINED ALL-PASS NETWORKS REALIZED BY DIGITAL SIGNAL PROCESSING MEANS~~

~~MAGNITUDE RESPONSE OF DIFFERENTIALLY COMBINED ALL-PASS NETWORKS~~

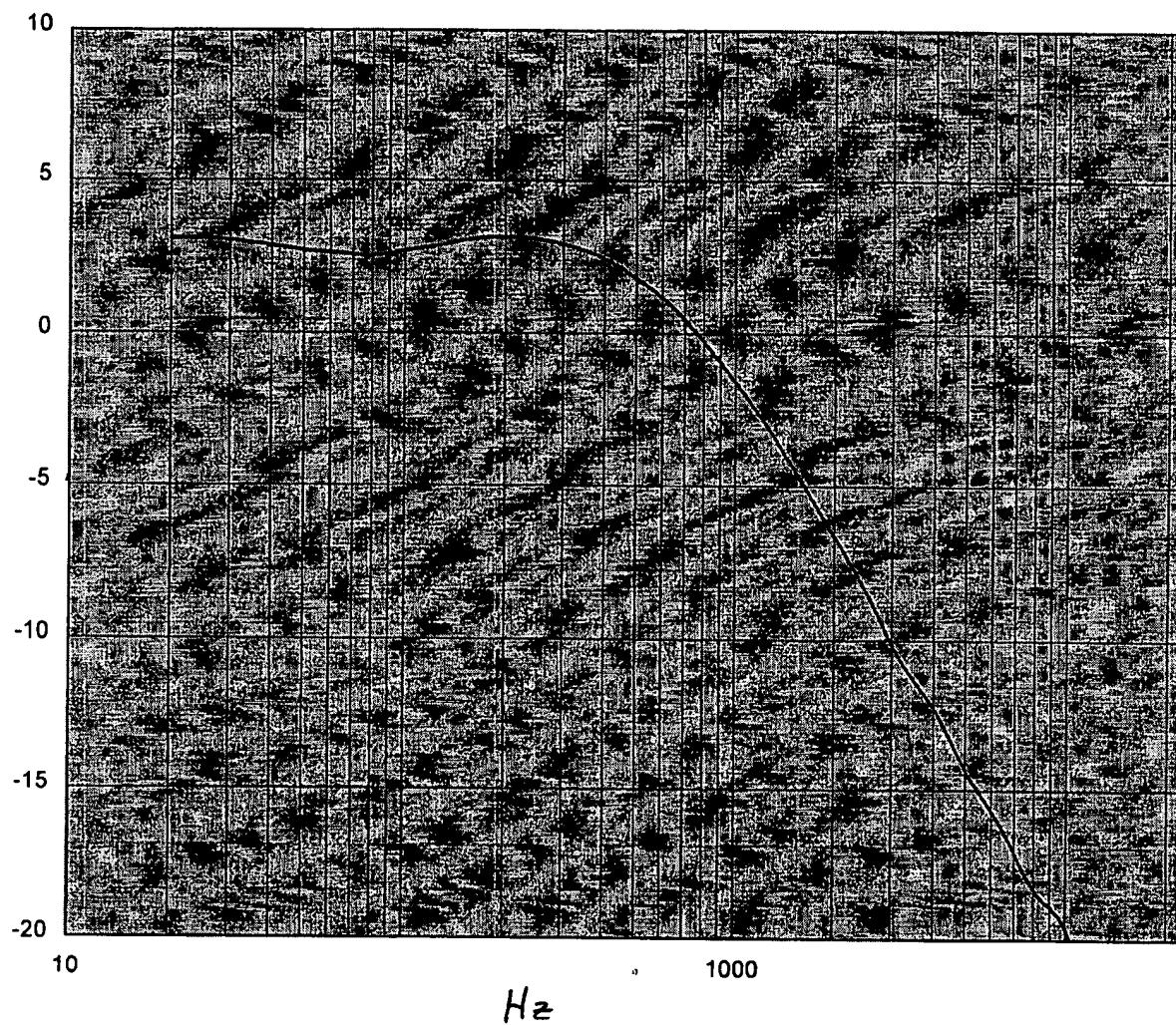


FIG. 7b

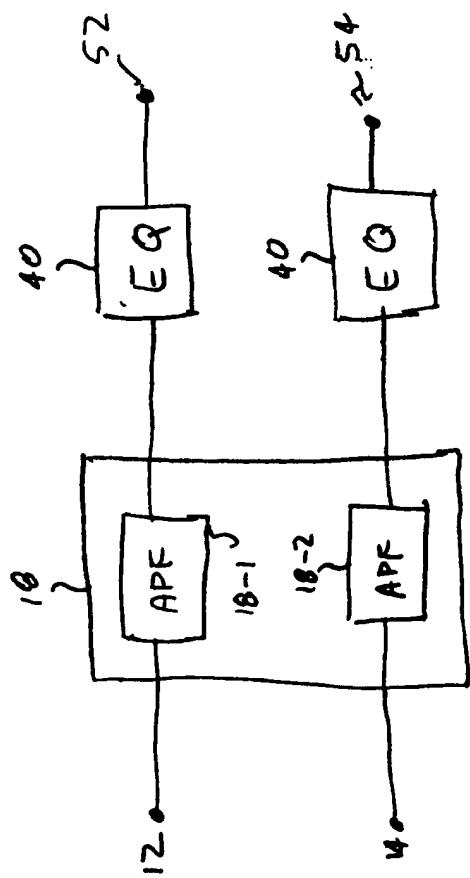


Fig. 8a

~~RELATIVE PHASE DIFFERENCE OF ALL PASS NETWORKS GIVEN  
IN OEM PROJECT~~

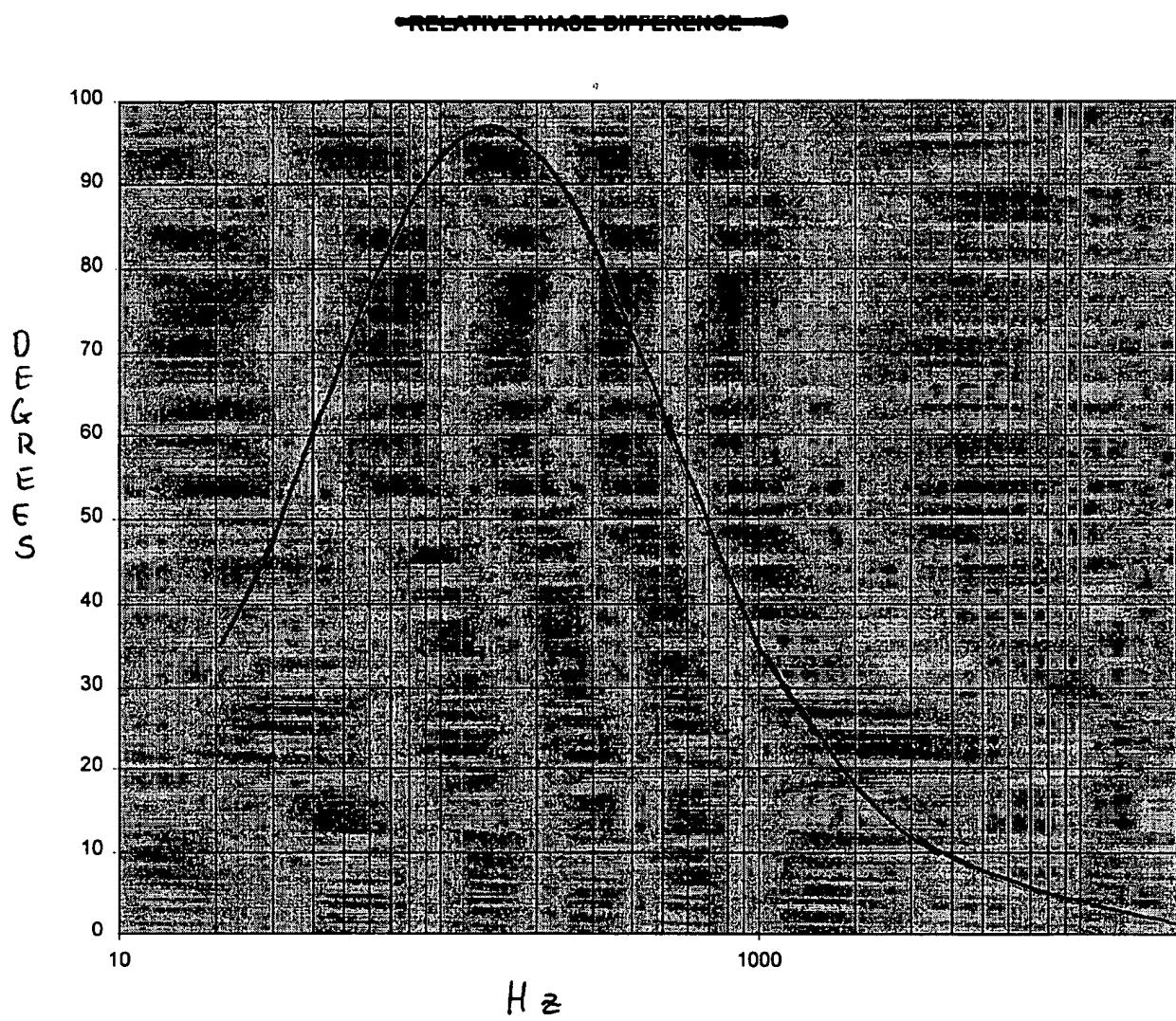


FIG. 8b

